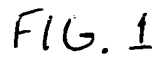


[illegible]

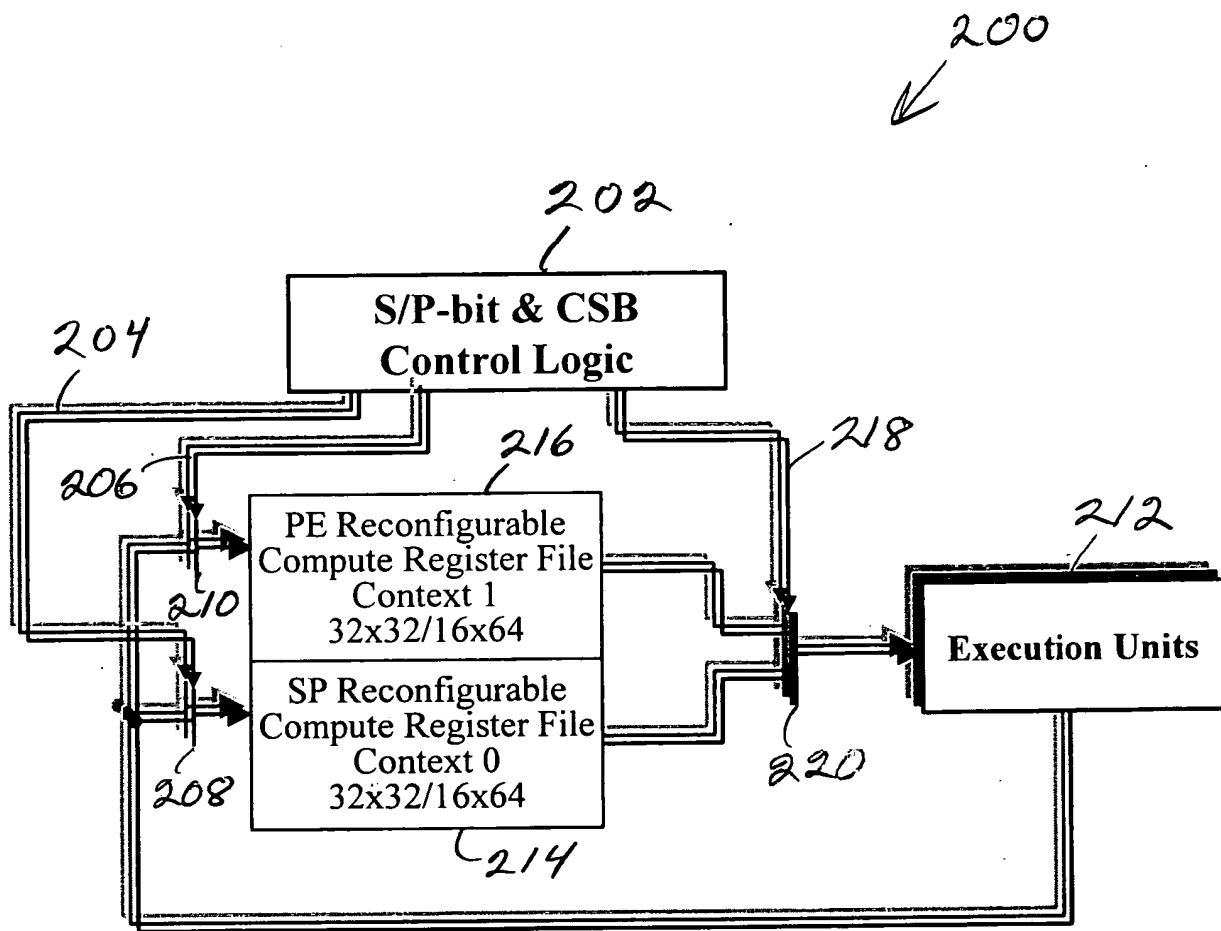


FIG. 2

S/P	ManArray Architecture Instruction Type
0	SP only instruction
1	PE only instruction

	Physical MxN	CSB	S/P	Operating MxN	Comments
320	1x1	0	0	1x1	SP Instructions use only the SP's context-0 register files
322	1x1	0	1	1x1	PE Instructions use only the PE0's register files.
324	1x1	1	0	1x0	SP Instructions use only the PE0's context-1 register files
326	1x1	1	1	1x0	PE Instructions typically not used.
330	1x2	0	0	1x2	SP Instructions use only the SP's context-0 register files
332	1x2	0	1	1x2	PE Instructions use only the PE0's and PE1's register files.
334	1x2	1	0	1x1	SP Instructions use only the PE0's context-1 register files
336	1x2	1	1	1x1	PE Instructions use only PE1's register files.
340	1x5	0	0	1x5	SP Instructions use only the SP's context-0 register files
342	1x5	0	1	1x5	PE Instructions use only PE0-4's register files.
344	1x5	1	0	2x2	SP Instructions use only the PE0's context-1 register files.
346	1x5	1	1	2x2	PE Instructions use only PE1-4's register files.

FIG. 3

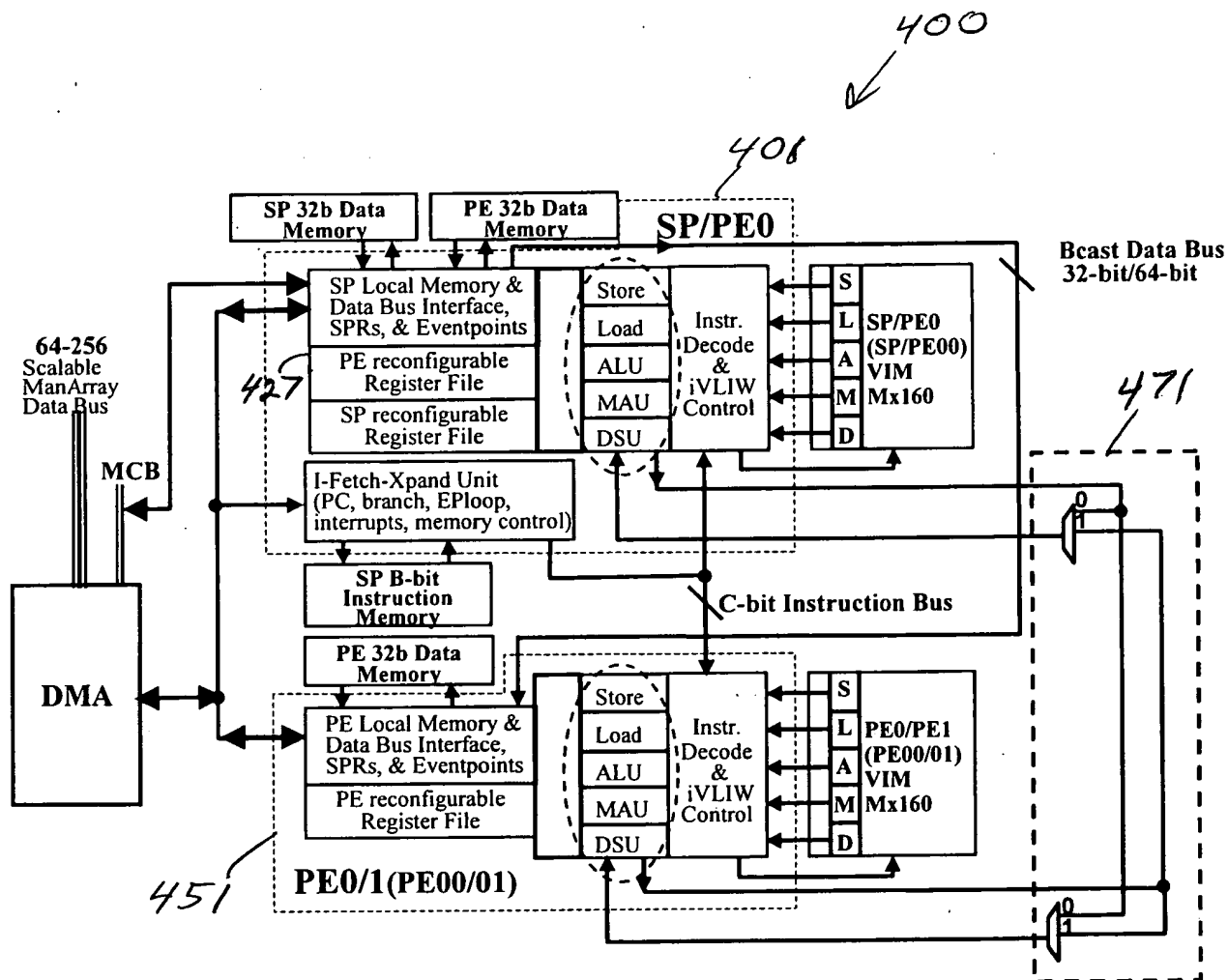


FIG. 4

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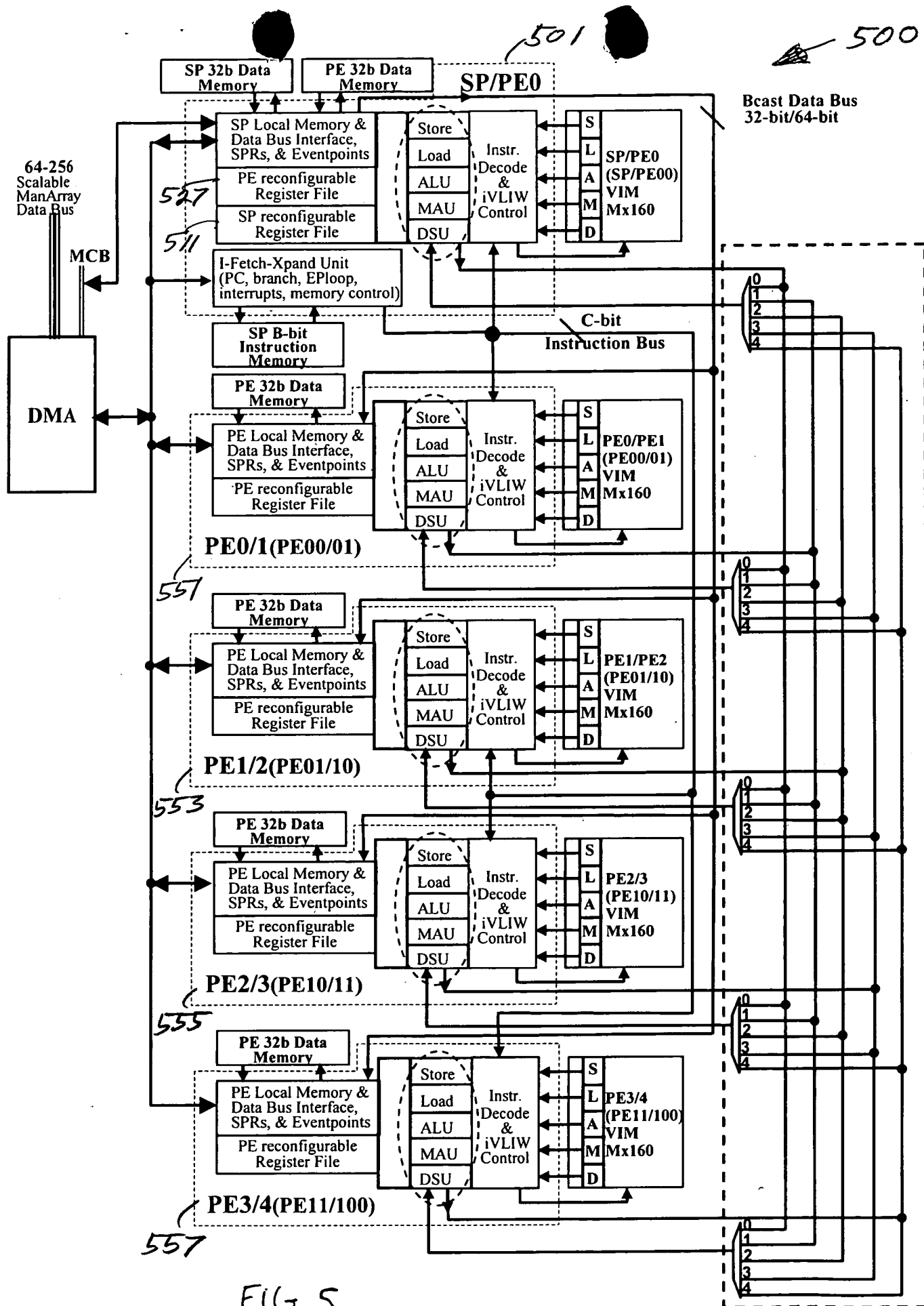


FIG. 5